

Infineon Docket No. 2003P52593US
Attorney Docket No. INFN/0019
Express Mail No. EV335468375US

**METHOD AND CIRCUIT CONFIGURATION FOR DIGITIZING
A SIGNAL IN AN INPUT BUFFER OF A DRAM DEVICE**

ABSTRACT

A method and circuit configuration for digitizing data and control signals using an input buffer in a dynamic random access memory (DRAM) device. In one embodiment, the input buffer includes buffer modules having a differential amplifier with a first input responsive to an input signal and a second input responsive to a reference voltage, a common source stage, and an output stage and a source of a bias voltage controlling impedance of the common source stage, wherein the reference voltage defines the amplitude of the bias voltage.